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## Patent Abstracts of Japan

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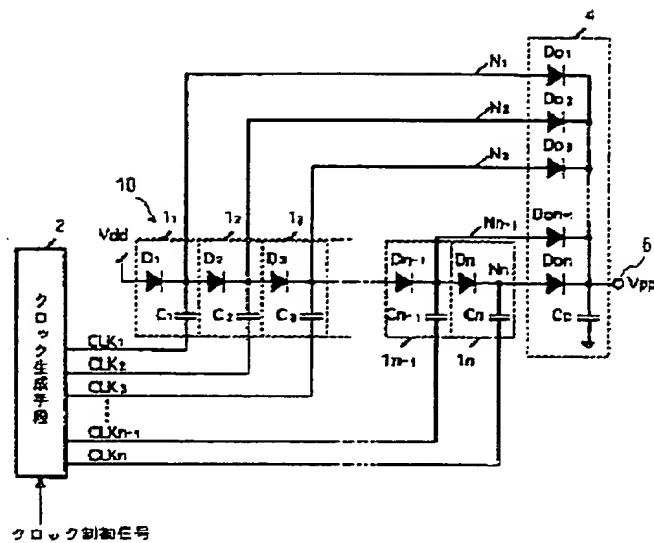
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TITLE : SEMICONDUCTOR STEP-UP CIRCUIT



ABSTRACT : PROBLEM TO BE SOLVED: To make it possible to selectively take out the output voltage of a plurality of levels and reduce power consumption by a simple circuit configuration.

SOLUTION: This step-up circuit is provided with a charge pump means 10 which, being constituted of a series combination of a plurality of pump blocks 1\_1-1\_n for conducting a step-up operation by charging and discharging capacitive elements C\_1-C\_n by means of step-up clocks CLK\_1-CLK\_n, generates the step-up voltage, a clock-generating means 2 for supplying the step-up clocks CLK\_1-CLK\_n to the pump blocks 1\_1-1\_n which constitute the charge pump means 10, and an output rectifying means 4 which connects at least one of the outputs of the pump blocks 1\_1-1\_n of the charge pump means 10 to a capacitive element C\_0 via the rectifying diodes D\_01-D\_0n.

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